XR-C200MK2/C202MK2

SERVICE MANUAL

AEP Model
UK Model
XR-C200MK2
German Model
XR-C202MK2

Photo : XR-C202MK2

Model Name Using Similar Mechanism	XR-C200/C202
Tape Transport Mechanism Type	MG-50EX-39

SPECIFICATIONS

Cassette player section

Tape track	4-track 2-channel stereo
Wow and flutter	0.08 % (WRMS)
Frequency response	30 – 18,000 Hz
Signal-to- noise ratio	
Cassette type	
TYPE II, IV	61 dB
TYPE I	58 dB

Tuner section

FM

Tuning range	87.5 – 108.0 MHz
Antenna terminal	External antenna connector
Intermediate frequency	10.7 MHz
Usable sensitivity	8 dBf
Selectivity	75 dB at 400 kHz
Signal-to-noise ratio	65 dB (stereo), 70 dB (mono)
Harmonic distortion at 1 kHz	
	0.5 % (stereo), 0.3 % (mono)
Separation	35 dB at 1 kHz
Frequency response	30 – 15,000 Hz
Capture ratio	2 dB

MW/LW (SW, for the XR-C202MK2)

Tuning range	MW: 531 – 1,602 kHz LW: 153 – 281 kHz SW: 5,950 – 6,205 kHz
Antenna terminal	External antenna connector
Intermediate frequency	10.71 MHz/450 kHz
Sensitivity	MW: 30 µV LW: 50 µV SW: 50 µV

Power amplifier section

Outputs	Speaker outputs (sure seal connectors)
Speaker impedance	4 – 8 ohms
Maximum power output	22 W × 4 (at 4 ohms)

General

Output lead	Power antenna relay control lead Power amplifier control lead (XR-C202MK2)
Tone controls	Bass ±8 dB at 100 Hz Treble ±8 dB at 10 kHz
Power requirements	12 V DC car battery (negative ground)
Dimensions	Approx. 188 × 58 × 170 mm (w/h/d) not incl. projecting parts and controls
Mounting dimension	Approx. 182 × 53 × 153 mm (w/h/d) not incl. projecting parts and controls
Mass	Approx. 1.3 kg
Supplied accessories	S Commander RM-X1S (XR-C200MK2 : AEP, C202MK2) (1) Power connecting cord (1) Mounting hardware (1 set) Chassis ground cord (1) Front panel case (1)

Design and specifications are subject to change without notice.

FM/MW/LW CASSETTE CAR STEREO
XR-C200MK2

FM/MW/SW CASSETTE CAR STEREO
XR-C202MK2

SONY®

SECTION 6
DIAGRAMS

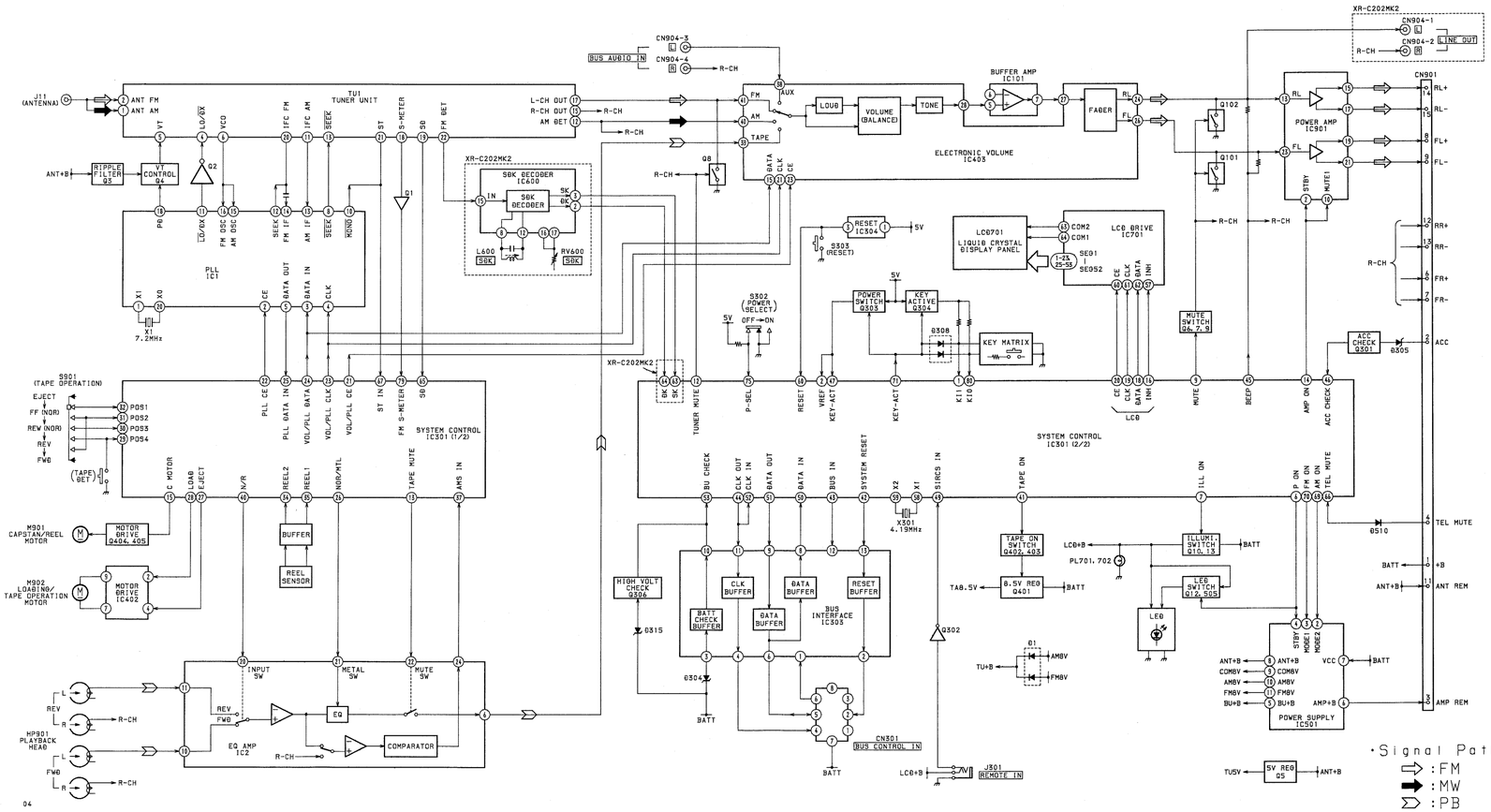
6-1. IC PIN DESCRIPTION

• IC301 μPD75518GF-304-3B9 (System Control)

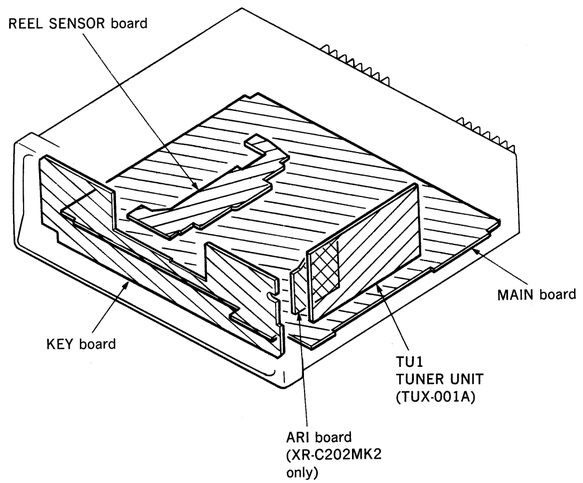
Pin No.	Pin Name	I/O	Pin Description
1	KI0	I	KEY input
2	VREF	I	A/D reference voltage input
3, 4	VDD	—	Power supply terminal
5	NC	—	No connection
6	P-ON	O	At P-ON : High output
7	ILL-ON	O	At ACC-ON : High output (P-SEL : ON) At P-ON : High output (P-SEL : OFF)
8	COLOR	—	No used.
9	MUTE	O	At MUTE : High output
10	AUX-MUTE	—	No used.
11	DOLBY-ON/OFF	—	Not used.
12	TUNER-MUTE	O	Except for TUNER : High output
13	TAPE-MUTE	O	Except for TAPE PLAYBACK : High output (At movement on FF, REW and AMS : Low output)
14	AMP-ON	O	Inner power amplifier control terminal. At AMP-ON : High output
15	C-MOTOR	O	Capstan motor control terminal. At MOTOR-ON : High output
16	LCD-INH	O	INHIBIT signal to LCD driver IC. At Low output : Light off
17	TEST MODE	I	At Low input : TEST MODE 1. Input available at timing only of RESET L to H and BU CHECK L to H.
18	LCD-DATA	O	DATA output terminal to LCD driver IC.
19	LCD-CLK	O	CLOCK output terminal to LCD driver IC.
20	LCD-CE	O	LATCH output terminal to LCD driver IC.
21	VOL-CE	O	LATCH output terminal to VOL IC.
22	PLL-CE	O	LATCH output terminal to PLL IC.
23	VOL. PLL-CLK	O	CLOCK output terminal to VOL and PLL IC.
24	VOL. PLL-DATA	O	DATA output terminal to VOL and PLL IC.
25	PLL DATA-IN	I	DATA input terminal from PLL IC.
26	NOR/MTL	I/O	At AUTO METAL : METAL input terminal. At METAL : Low input, at NORMAL : High input. At no AUTO METAL : METAL output terminal. At METAL : High output, at NORMAL : Low input.
27	EJECT	O	LOADING MOTOR control terminal. At moving in the direction of EJECT : High output.
28	LOAD	O	LOADING MOTOR control terminal. At moving in the direction of LOAD and PLAY.
29—32	POS4—POS1	I	MD position detection terminal
33	GND	—	GND
34	REEL2	I	MD reel table rotation detection terminal. FWD supply side
35	REEL1	I	MD reel table rotation detection terminal. FWD take-up side
36	AUTO-MTL	—	Connect to GND.
37	AMS-IN	I	TAPE music with/without detection terminal. Low input : With music, High input : Without music
38	EEPROM-DATA	—	Connect to GND.
39	EEPROM-CLK	—	Not used.
40	N/R	O	NORMAL/$\overline{\text{REVERSE}}$ output terminal
41	TAPE-ON	O	OR output of LM•EJECT and LM•LOAD. At one side ACTIVE : High output, at REEL detection : High output
42	SYSTEM RESET	O	UNILINK $\overline{\text{SYSTEM RESET}}$ terminal. Low output : SYSTEM RESET

Pin No.	Pin Name	I/O	Pin Description
43	BUS-IN	O	For BUS CONTROL terminal
44	CLK-OUT	O	For BUS CONTROL terminal
45	BEEP	O	For piezoelectric buzzer output terminal
46	ACC-CHECK	I	Accessory detection terminal, Low input : ACC ON
47	KEY-ACK	I	KEY ACKNOWLEDGE input terminal. For KEY insert input
48	C-ALARM	I	Caution alarm with/without initial setting terminal. Low input : With caution alarm
49	SIRCS-IN	I	SIRCS (REM DET) input terminal
50	DATA-IN	I	For BUS CONTROL terminal
51	DATA-OUT	O	For BUS CONTROL terminal
52	CLK-IN	I	For BUS CONTROL terminal
53	BU. CHECK	I	Back Up voltage detection terminal
54	VSS	—	GND
55	XT1	—	Connect to GND.
56	XT2	—	Not used.
57	IC	—	Connect to GND.
58	X1	—	Connect to crystal. (4.19MHz)
59	X2	—	Connect to crystal. (4.19MHz)
60	RESET	I	RESET input
61, 62	—	—	Connect to GND.
63	SK	I	High input : With SK
64	DK	I	High input : With DK
65	SD	I	High input : With SD
66	TEL-MUTE	I	Low input : 20dB audio mute
67	ST-IN	I	Low input : Stereo
68	N-SW	I	Low input : With front panel
69	AM-ON	O	At TUNER ON : High output
70	FM-ON	O	At FM (both playing and behind) : High output
71	KEY-ACT	O	Reverse pin ⑤ and Active output terminal
72	—	—	Not used.
73	A. GND	—	A/D GND
74	DOLBY. SEL	I	Connect to GND.
75	P-SEL	I	POWER SELECT switch input. High input : ON, Low input : OFF. (Low input : Setting without ACC position)
76	DEST1	I	Destination setting terminal
77	DEST0	I	Destination setting terminal
78	TPMODE	I	TP MODE input terminal
79	S-METER	I	FM S meter input terminal
80	KI1	I	KEY input terminal

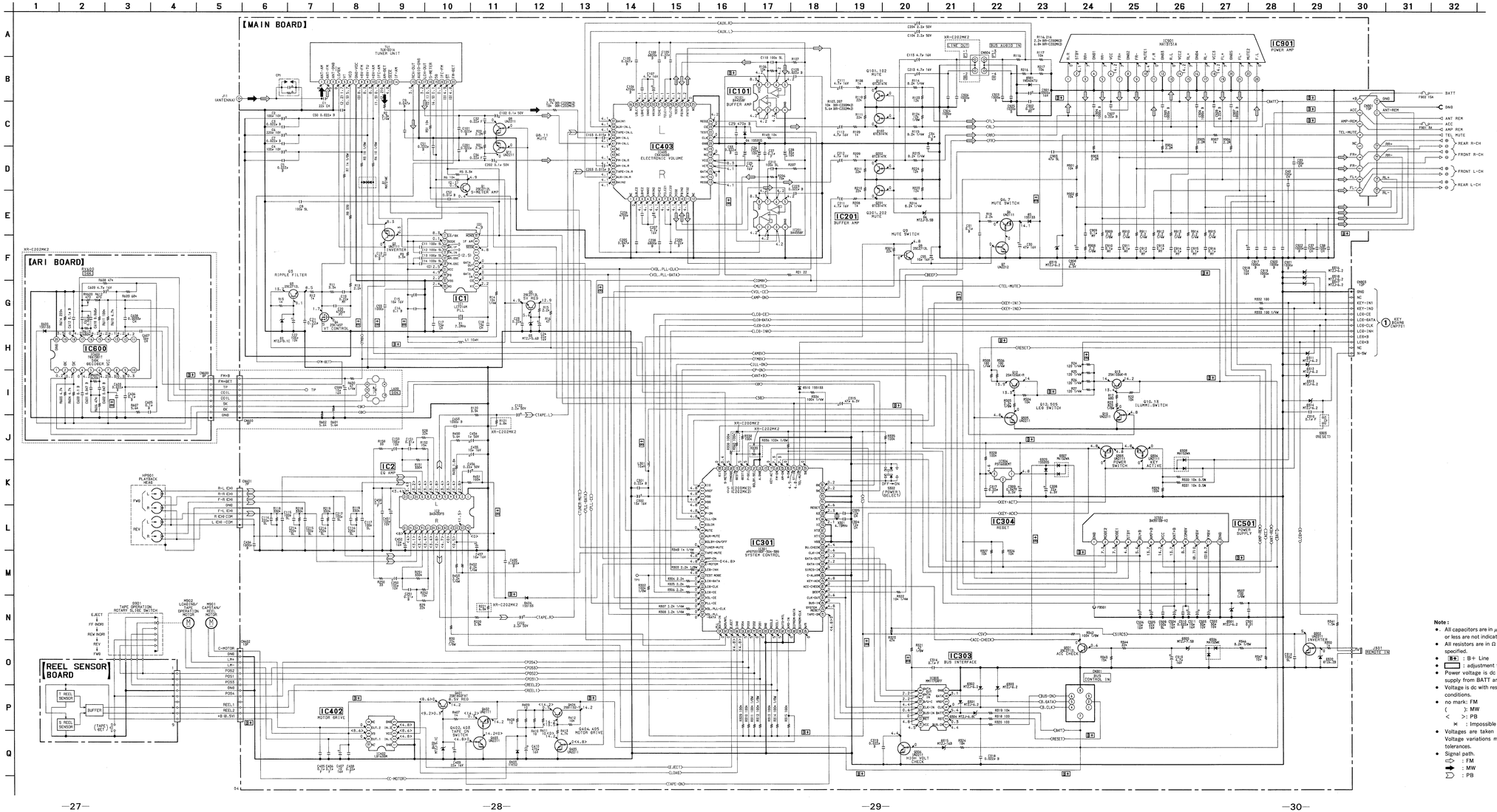
6-2. BLOCK DIAGRAM



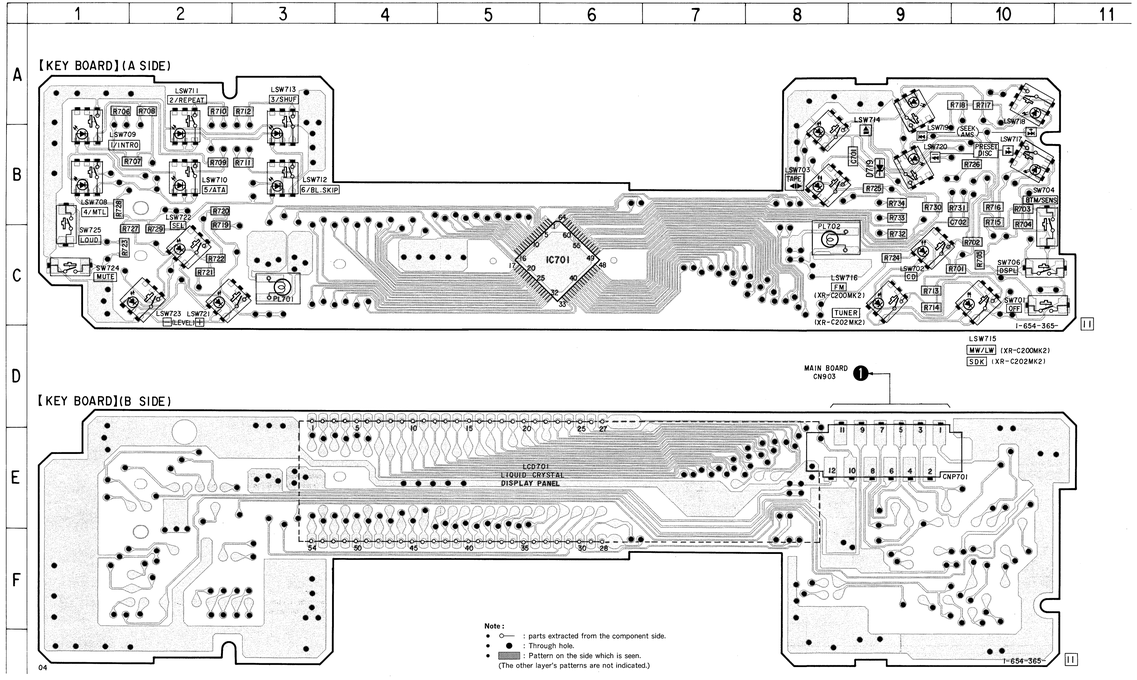
6-3. CIRCUIT BOARDS LOCATION



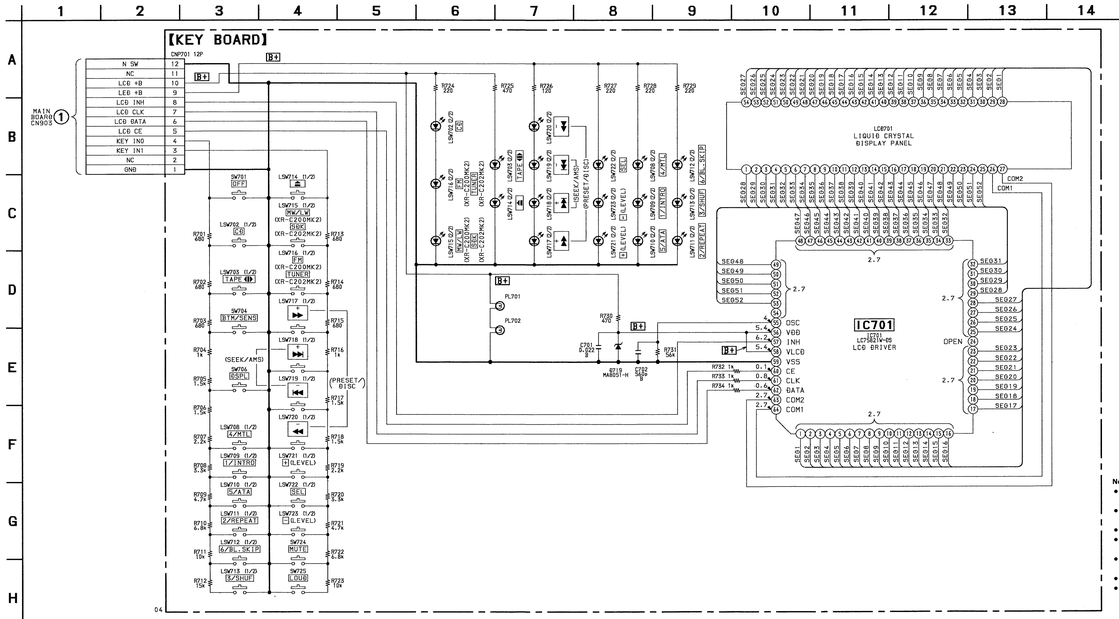
6-5. SCHEMATIC DIAGRAM—MAIN SECTION— • Refer to page 37 for IC Block Diagrams.


【MAIN BOARD】
【ARI BOARD】
【REEL SENSOR BOARD】
IC1
IC2
IC101
IC201
IC301
IC303
IC304
IC402
IC403
IC501
IC600
IC801
Note :
• All capacitors are in μF unless otherwise noted. pF : μμF 50WV or less are not indicated except for electrolytics and tantalums.
• All resistors are in Ω and 1/4 W or less unless otherwise specified.
• : B+ Line
• : adjustment for repair.
• Power voltage is dc 14.4V and fed with regulated dc power supply from BATT and ACC terminals.
• Voltage is dc with respect to ground under no-signal (detuned) conditions.
• no mark: FM
( ): MW
< >: PB
✕ : Impossible measurement point
• Voltages are taken with a VOM (Input Impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.
• Signal path.
: FM
: MW
: PB

6-6. PRINTED WIRING BOARDS—PANEL SECTION—



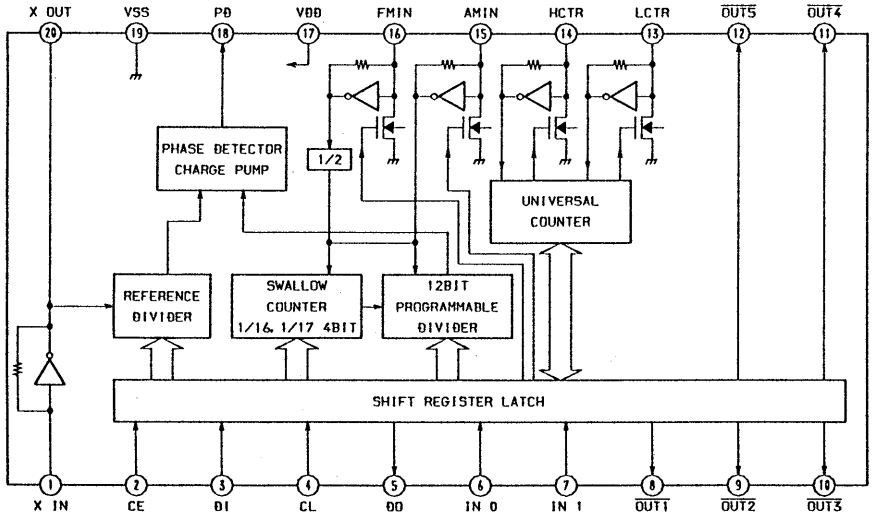
6-7. SCHEMATIC DIAGRAM—PANEL SECTION—



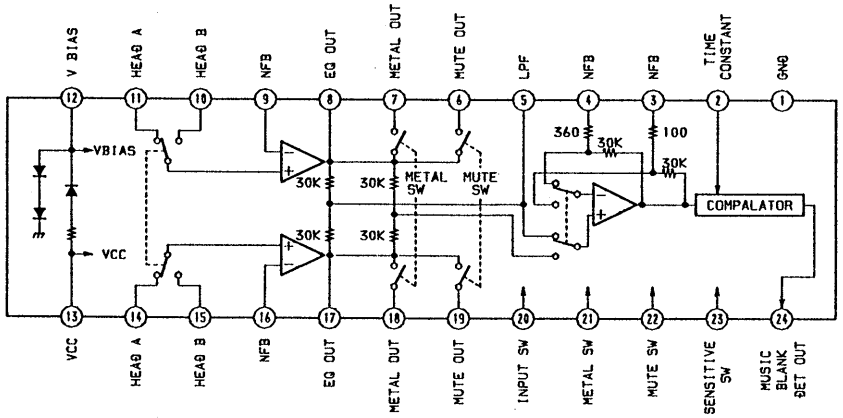
Note :
- All capacitors are in μF unless otherwise noted. pF : μμF 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- B+ : B+ Line
- Power voltage is dc 14.4V and fed with regulated dc power supply from BATT and ACC terminals.
- Voltage is dc with respect to ground under no-signal (detuned) conditions.
- no mark: FM
- Voltages are taken with a VOM (Input Impedance 10MΩ). Voltage variations may be noted due to normal production tolerances.

• IC Block Diagrams

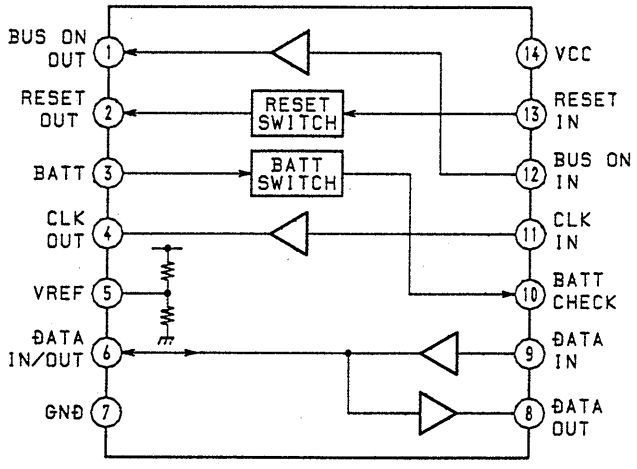
IC1 LC7216M



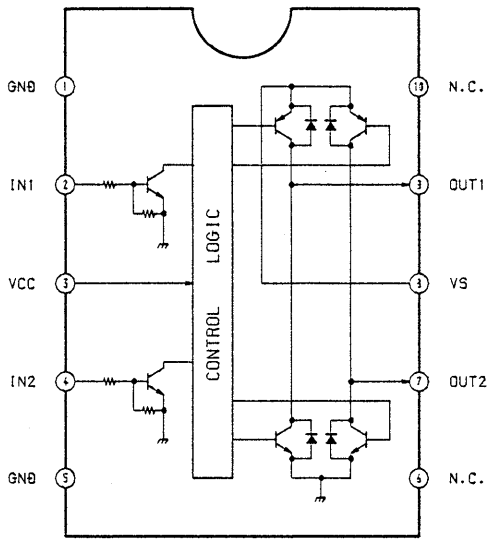
IC2 BA3430FS



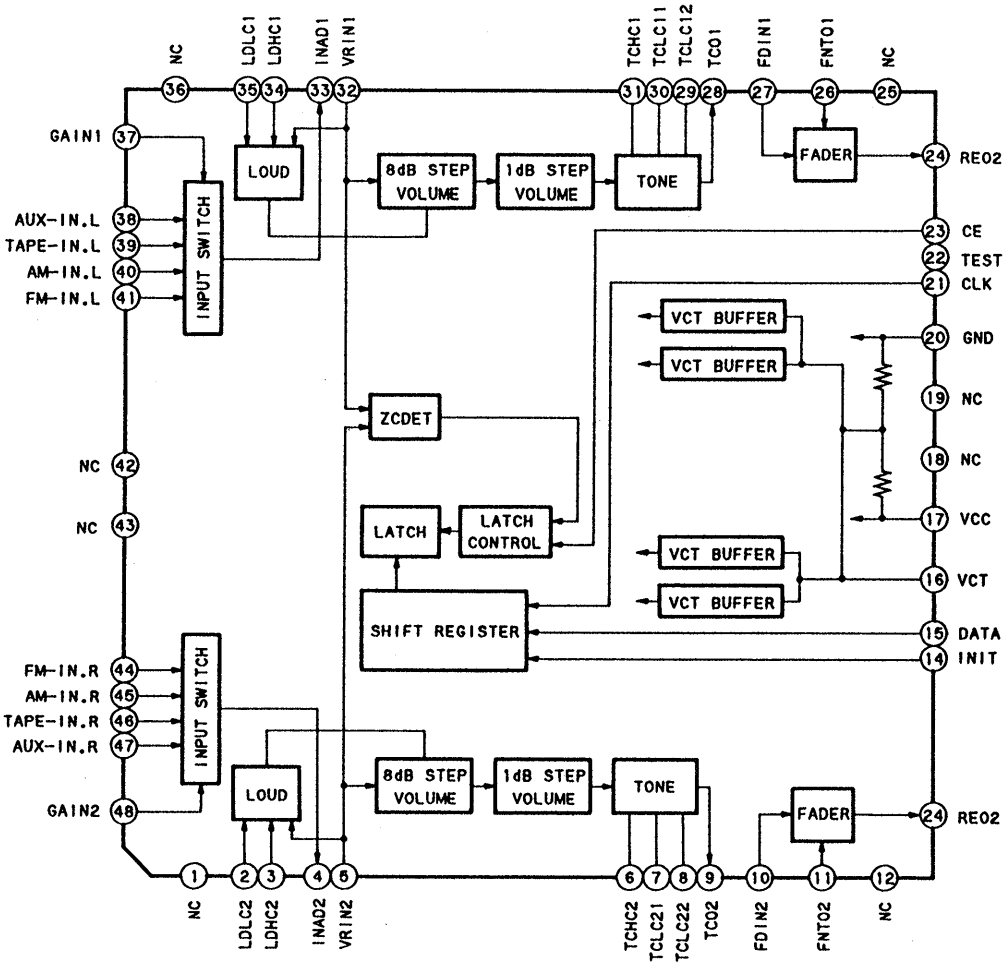
IC303 MM1175XFF



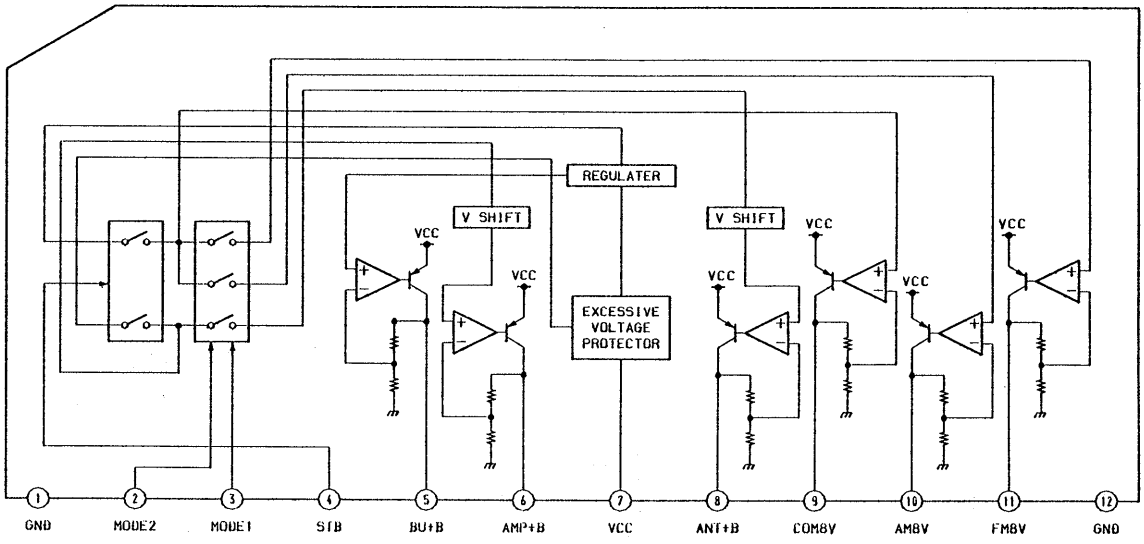
IC402 LB1638M



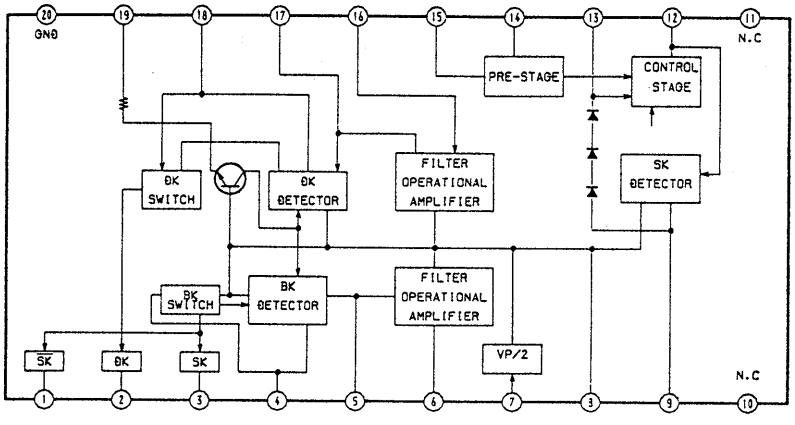
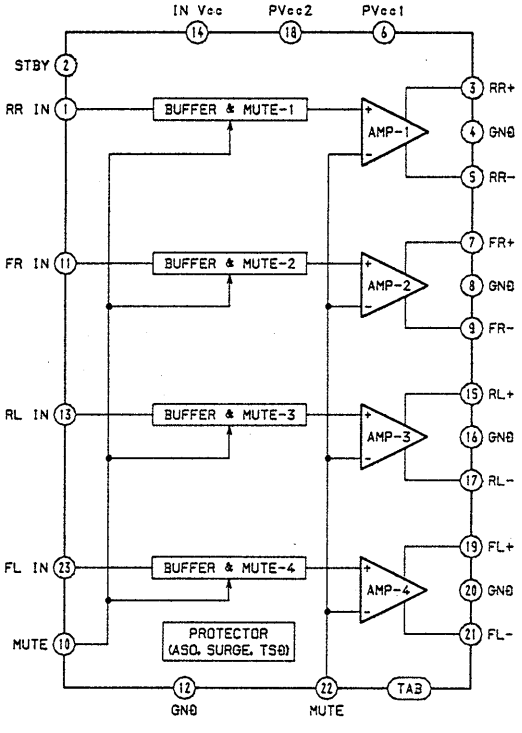
IC403 CXA1646Q



IC501 BA3910B-V2



IC600 TDA1581T (XR-C202MK2 only)



IC901 HA13151A